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Last Name = SHERIGAR

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Application#	Patent#	PG Pub#	Status	Date Filed	Title	Examiner Name	Inv Na
10060118	7508874	20030142752	150	01/29/2002	ERROR CONCEALMENT FOR MPEG DECODING WITH PERSONAL VIDEO RECORDING FUNCTIONALITY	LEE,Y	SH BH
10737492	Not Issued	20050129007	61	12/16/2003	Dual context audio parser	KAO,WEI PO	SH BH
10775652	Not Issued	20050175106	71	02/09/2004	Unified decoder architecture	RASHIDIAN,MOHAMMAD	SH BH
10880695	Not Issued	20050174269	61	06/29/2004	Huffman decoder used for decoding both advanced audio coding (AAC) and MP3 audio	ADESANYA,OLUJIMI	SH BH
11092256	Not Issued	20060227874	41	03/29/2005	System, method, and apparatus for DC coefficient transformation	HOLDER,ANNER	SH BH
11092285	7091888	0	150	03/29/2005	RUN-LEVEL AND COMMAND SPLIT FIFO STORAGE APPROACH IN INVERSE QUANTIZATION	MAIL,LAM	SH BH
11092339	Not Issued	20060227875	71	03/29/2005	System, and method for DC coefficient prediction	HOLDER,ANNER	SH BH
11092347	Not Issued	20060227865	71	03/29/2005	Unified architecture for inverse scanning for plurality of scanning scheme	HOLDER,ANNER	SH BH

11092932	Not Issued	20060227876	61	03/29/2005	System, method, and apparatus for AC coefficient prediction	TORRENTE,RICHARD	SH BH
11096604	Not Issued	20060222247	71	04/01/2005	Hardware implementation of inverse scan for a plurality of standards	GE,YUZHEN	SH BH
11096988	Not Issued	20060222064	71	04/01/2005	Hardware implementation of optimized single inverse quantization engine for a plurality of standards	AN,SHAWN	SH BH
11317578	Not Issued	20070147496	30	12/23/2005	Hardware implementation of programmable controls for inverse quantizing with a plurality of standards	DASTOURI,MEHRDAD	SH BH
12409439	Not Issued	0	20	03/23/2009	Error Concealment for MPEG Decoding with Personal Video Recording Functionality	,	SH BH
60542401	Not Issued		159	02/05/2004	Huffman decoder used for decoding both advanced audio coding (AAC) and MP3 audio	,	SH BH
10735980	7284072	20050038938	150	12/15/2003	DMA ENGINE FOR FETCHING WORDS IN REVERSE ORDER	FRANKLIN,RICHARD	SH BH M/
10736125	Not Issued	20050036614	41	12/15/2003	DMA engine for fetching words in reverse bit order	TSENG,CHENG YUAN	SH BH M/
60494666	Not Issued		159	08/13/2003	DMA state machine for fetching addresses in reverse order	,	SH BH M/
60494746	Not Issued		159	08/13/2003	DMA architecture for providing data words in reverse order	,	SH BH M/

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